



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/084,105

Filed: February 27, 2002

Inventor(s):

Andrew Phelps

Title: MEMORY SUBSYSTEM
INCLUDING AN ERROR
DETECTION MECHANISM
FOR ADDRESS AND
CONTROL SIGNALS

§ Examiner: Gabriel L. Chu
§ Group/Art Unit: 2114
§ Atty. Dkt. No: 5181-97700
§

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B. Noël Kivlin

Printed Name

March 3, 2005

Signature

Date

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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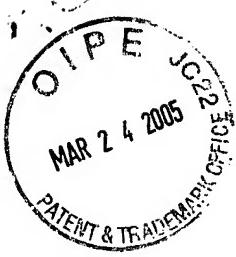
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Respectfully submitted,

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Date: March 3, 2005



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S): Andrew Phelps
ASSIGNEE: Sun Microsystems, Inc.
SERIAL NO.: 10/084,105
FILING DATE: February 27, 2002

TITLE: MEMORY SUBSYSTEM INCLUDING AN ERROR DETECTION MECHANISM FOR ADDRESS AND CONTROL SIGNALS

ATTORNEY DOCKET: 5181-97700 / P6683

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